

FIG. 1

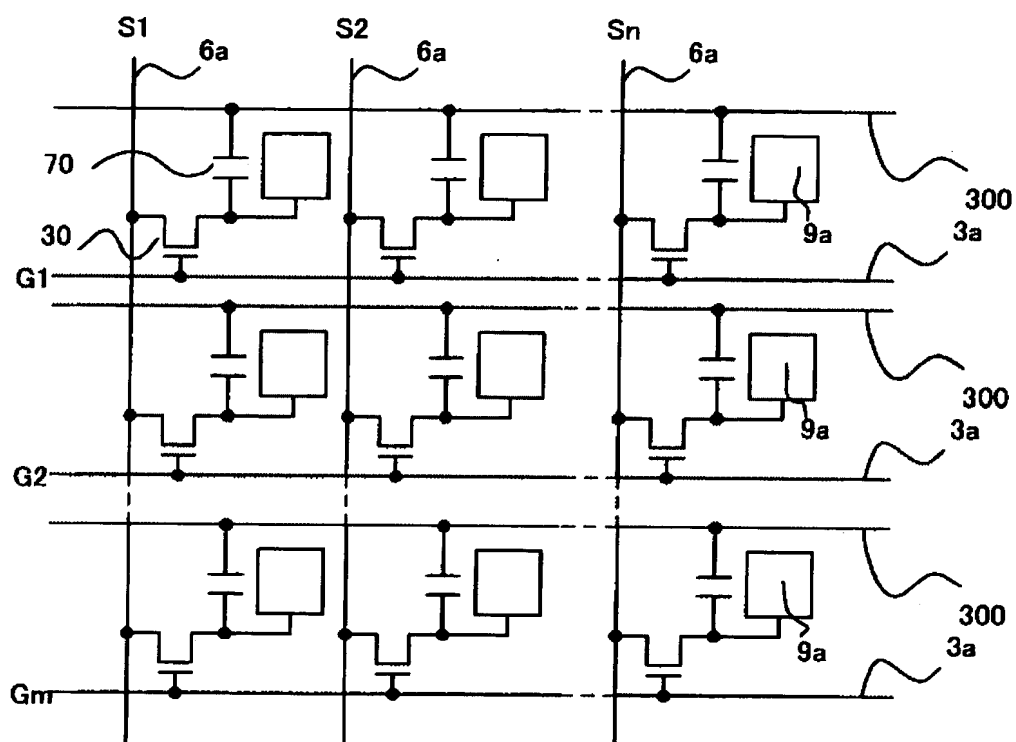


FIG. 1 is a schematic diagram of a multi-chambered device, likely a microfluidic chip or a multi-well plate. The device consists of a central rectangular area divided into four quadrants by a horizontal and a vertical channel. Each quadrant contains a rectangular chamber (300) with a central component (71). The chambers are connected to a network of channels (1a, 1a', 3a, 6a, 81, 83, 85, 11a) and are surrounded by a border (9a). The device is shown in a cross-sectional view, with various components labeled with reference numerals.

FIG. 3

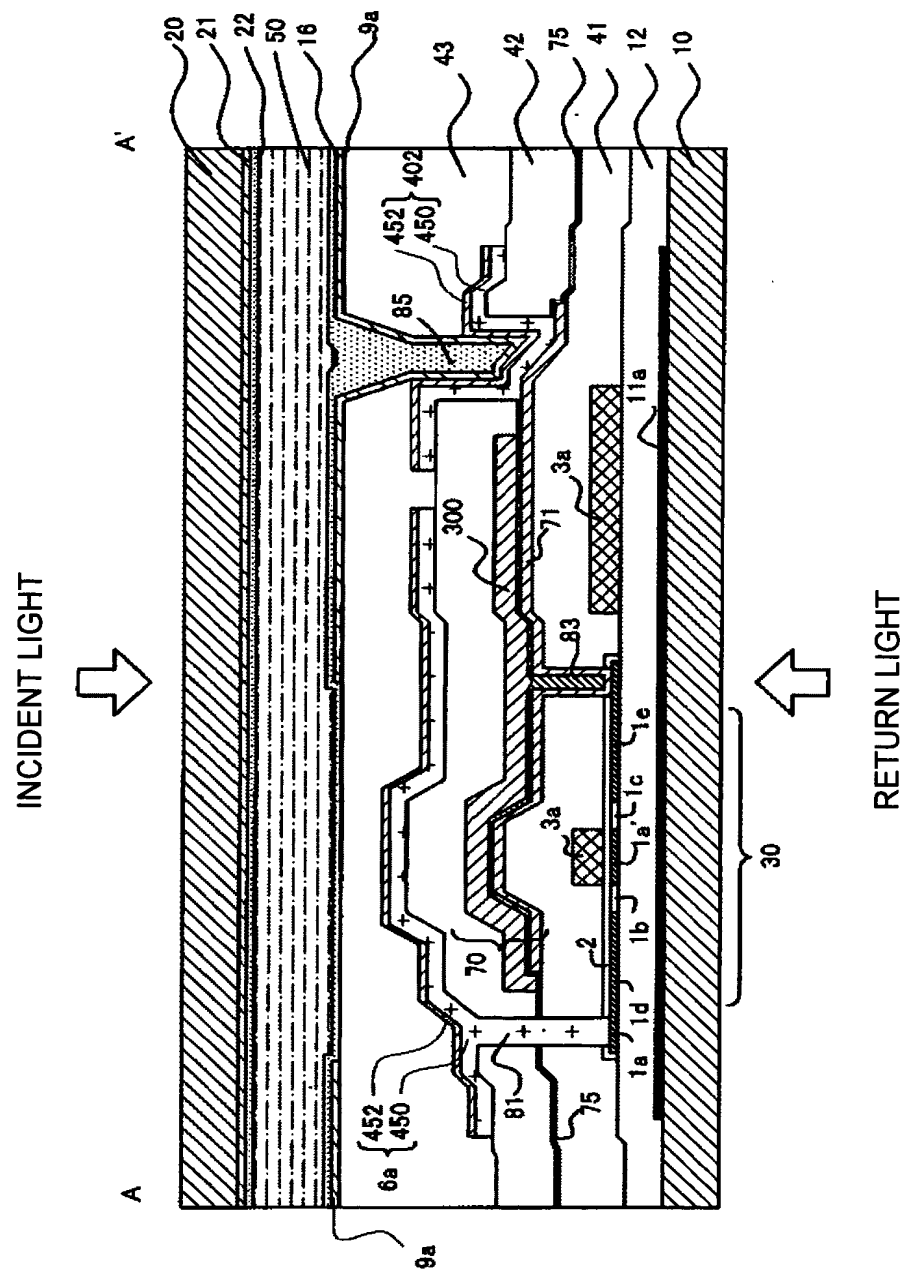


FIG. 4

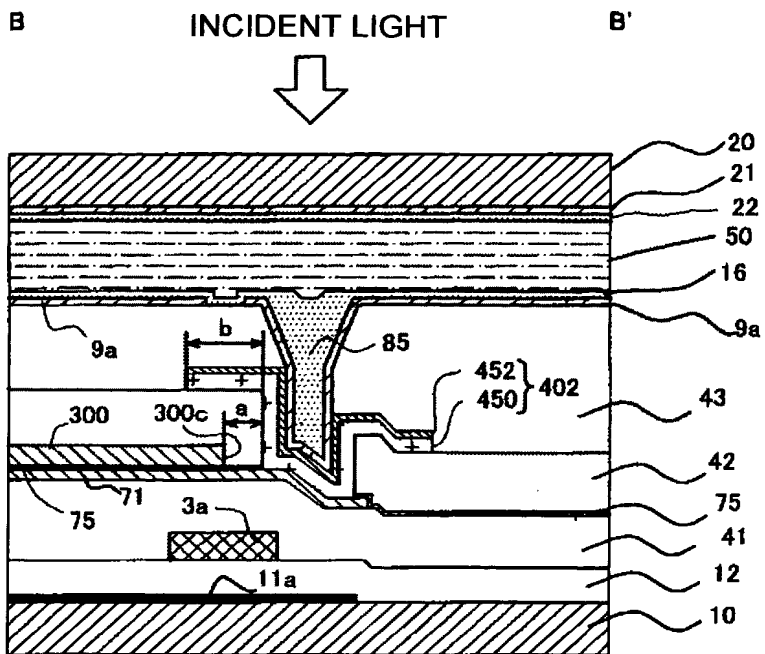


FIG. 5

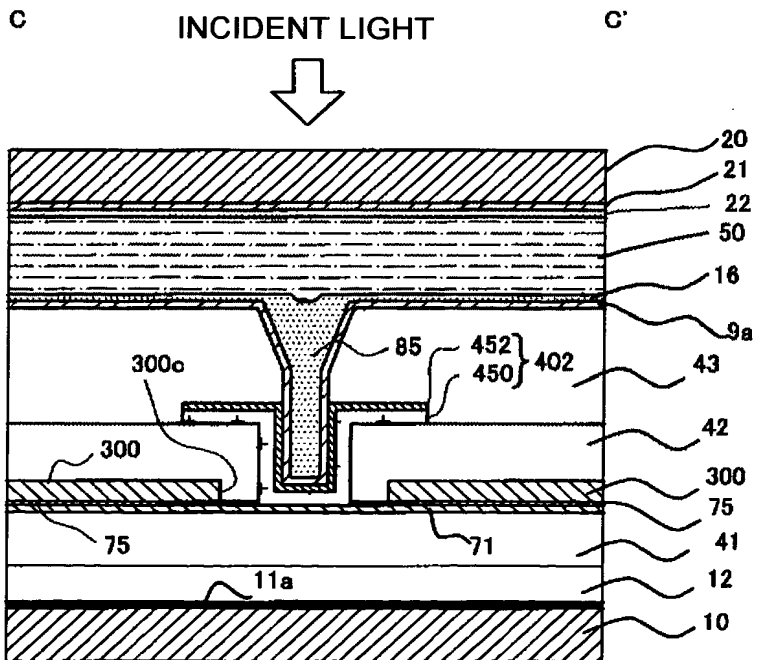


FIG. 6

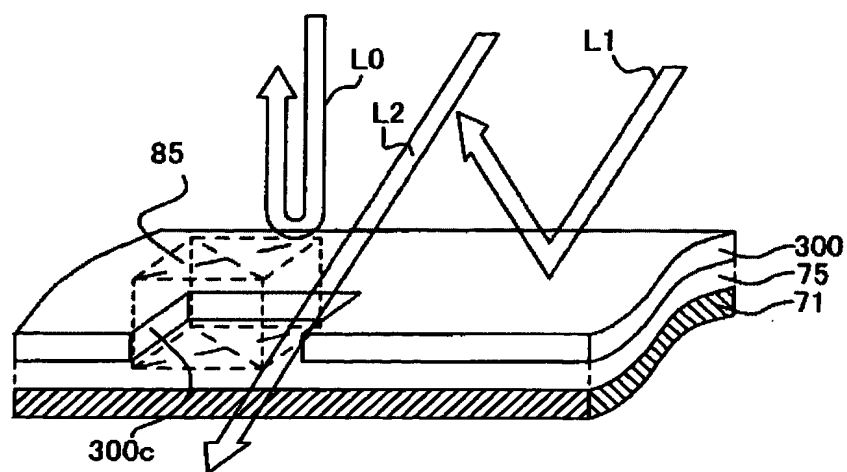


FIG. 7

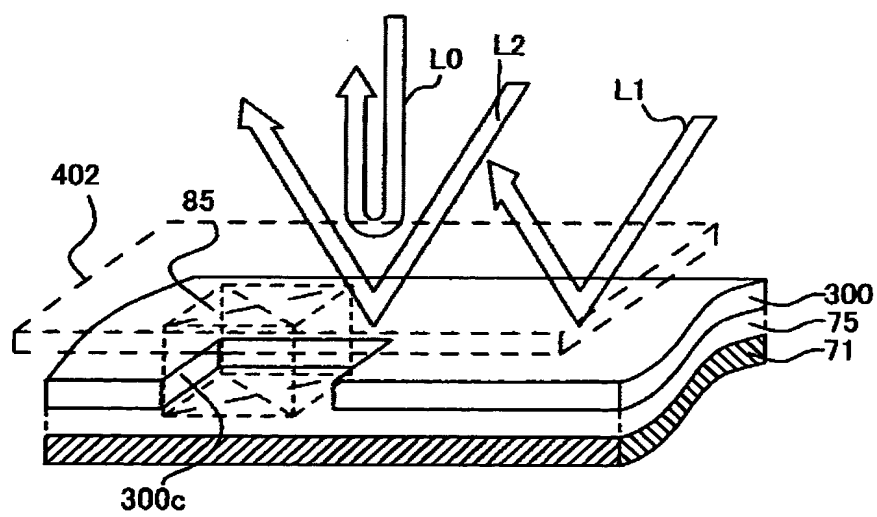


FIG. 8

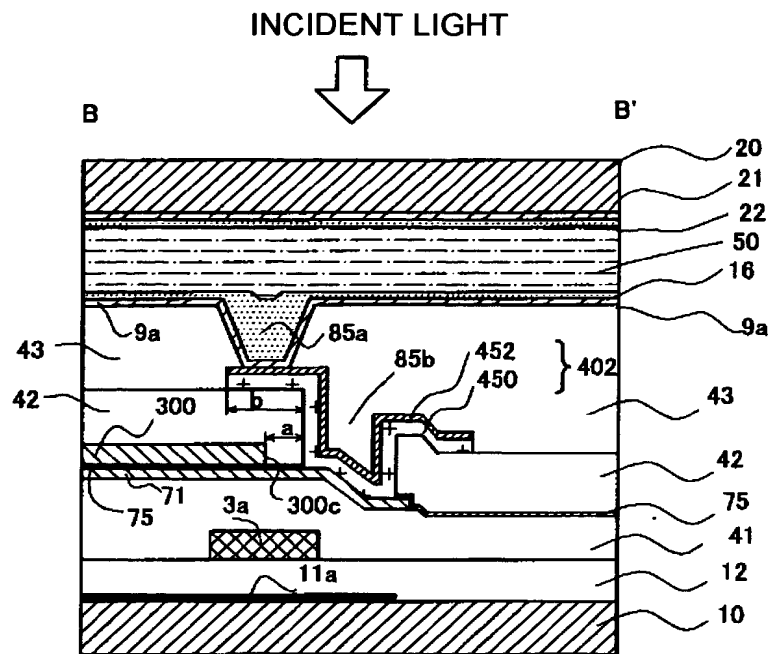


FIG. 9

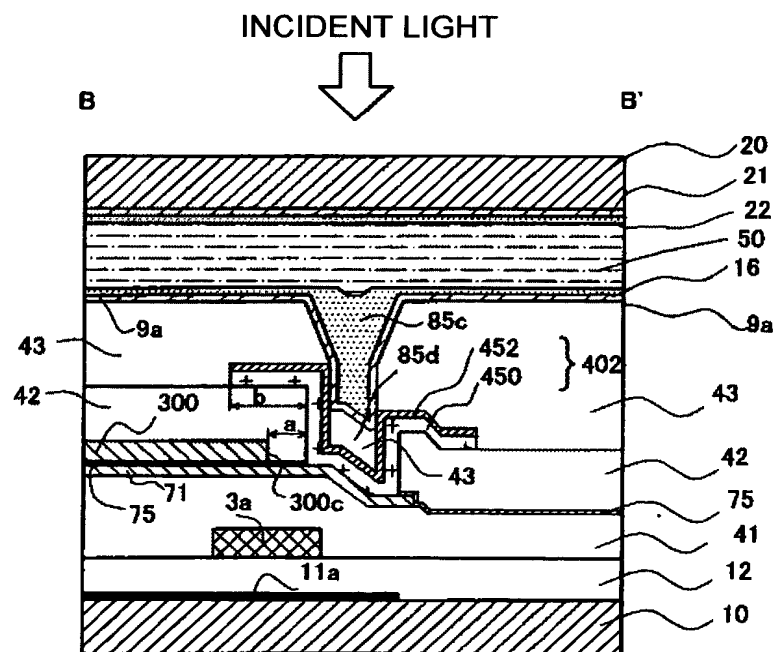


FIG. 10

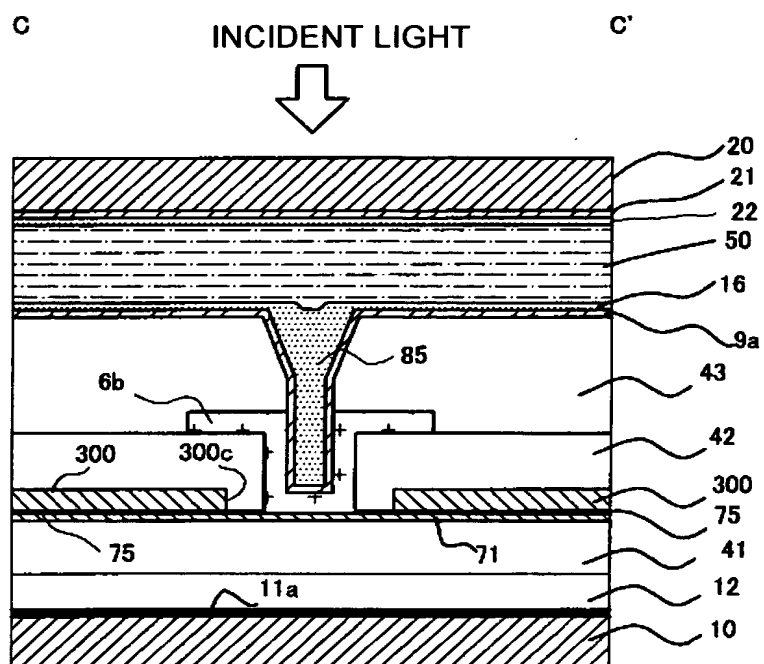


FIG. 11

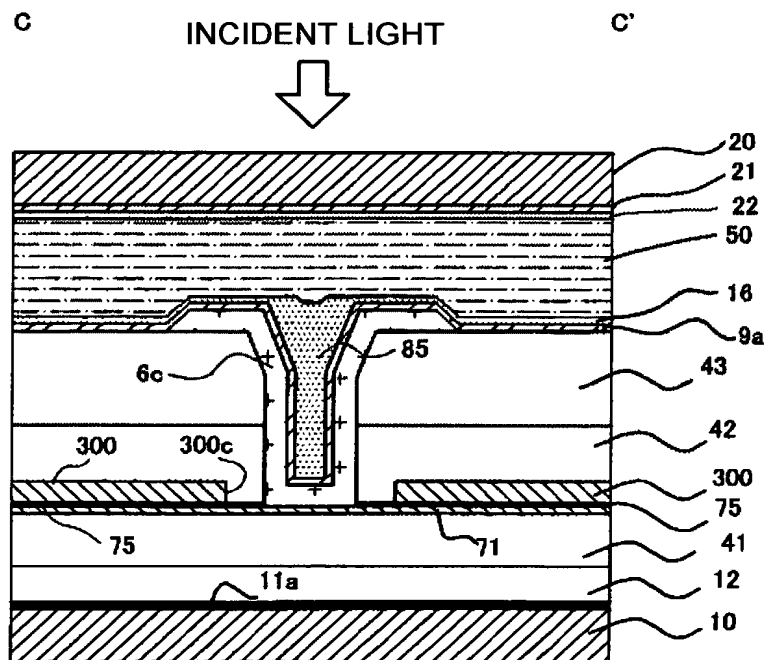


FIG. 12

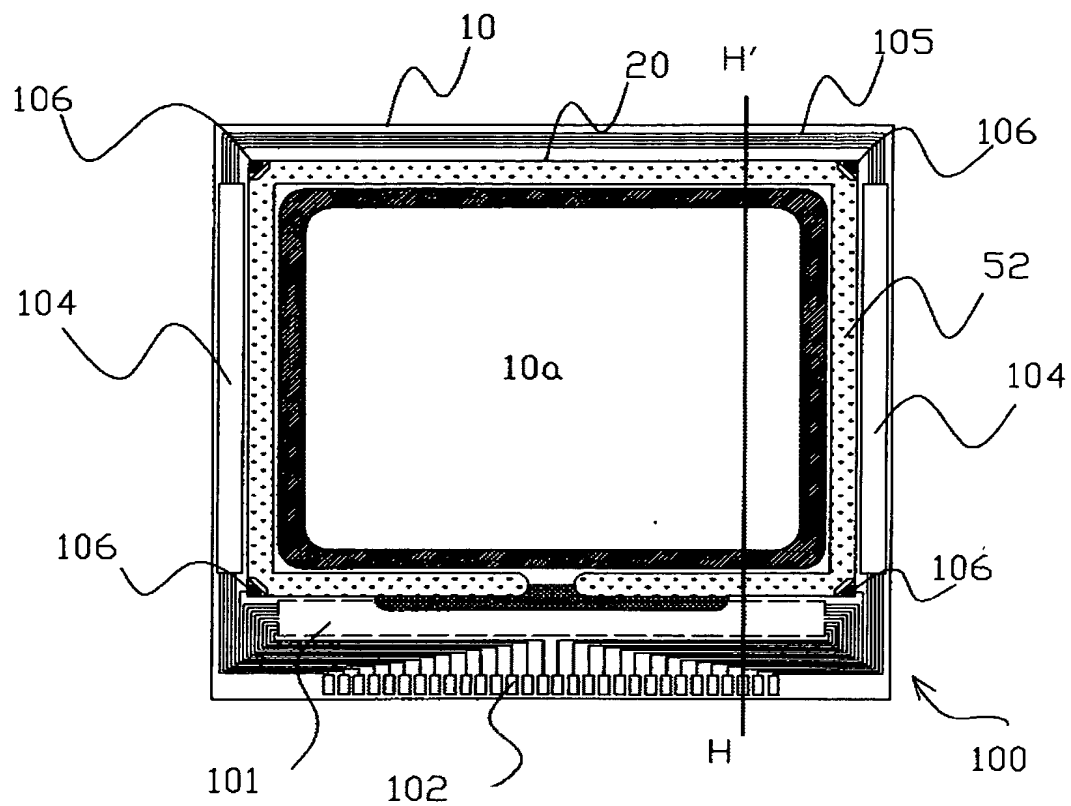


FIG. 13

